

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	972	delay adj element with (transistor\$1 FET\$1 MOSFET\$1)	USPAT; US-P GPUB ; EPO; JPO; DERW ENT	2004/09/2 3 07:16	
2	BRS	L2	343	1 and gate with delay adj element with (transistor\$1 FET\$1 MOSFET\$1)	USPAT; US-P GPUB ; EPO; JPO; DERW ENT	2004/09/2 3 07:17	
11	BRS	L11	183	2 and (stack\$3 cascod\$3 cascad\$3 stage\$1)	USPAT; US-P GPUB ; EPO; JPO; DERW ENT	2004/09/2 3 07:37	